

P6495a

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Yoshiaki Mori, et al.

Group Art Unit: 1796

Serial No.: 10/026,286

Examiner: Not Yet Assigned

Filed: December 20, 2001

Title: Pattern Forming Method And Apparatus Used For Semiconductor Device, Electric Circuit, Display Module, And Light Emitting Device

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on this date.

Date: June 27, 2002

  
Mary Bastida

REQUEST FOR APPROVAL TO MAKE DRAWING CHANGES

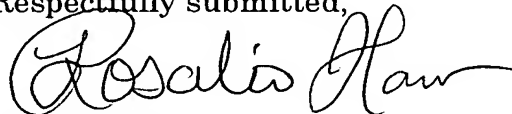
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Approval is respectfully requested to amend the drawings in the above-identified patent application as described below and as illustrated in red on the attached copies of the pertinent figures as filed:

Figs. 2, 5, 18 through 29 and 33 through 34, re-label as shown.

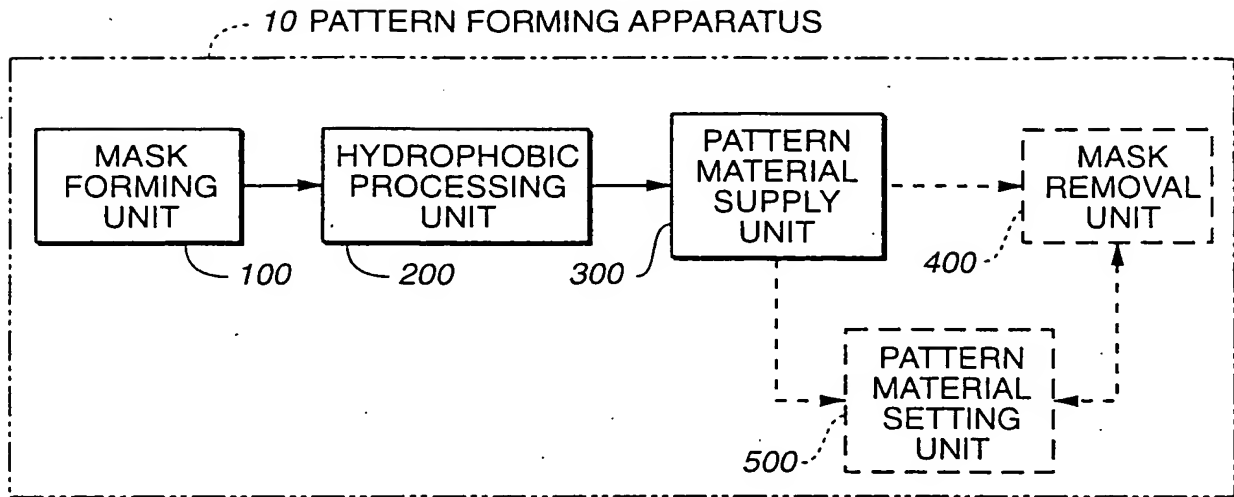
Respectfully submitted,



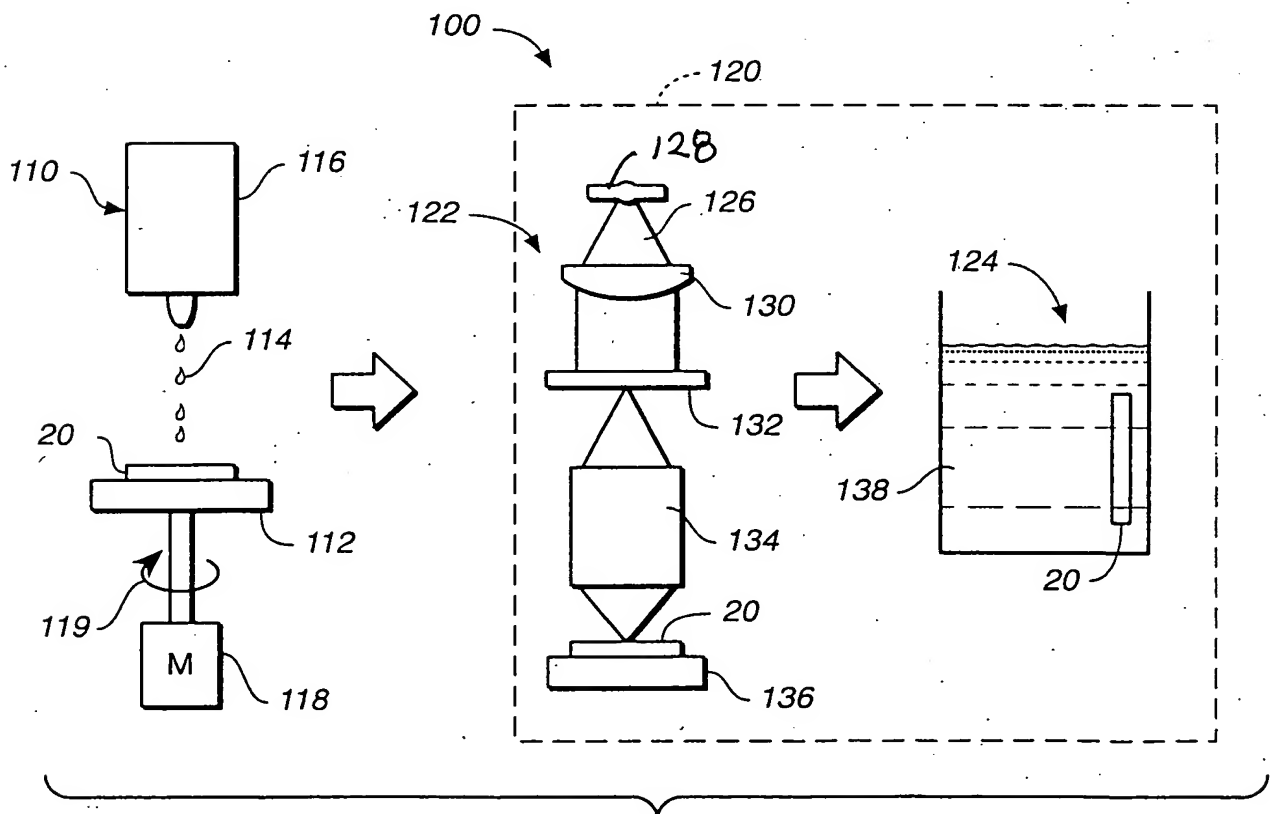
Rosalio Haro  
Attorney for Applicants  
Registration No. 42,633

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Intellectual Property Department  
150 River Oaks Parkway, Suite 225  
San Jose, CA 95134  
Customer No. 20178  
Phone: (408) 952-6000  
Fax: (408) 954-9058

Date: June 27, 2002

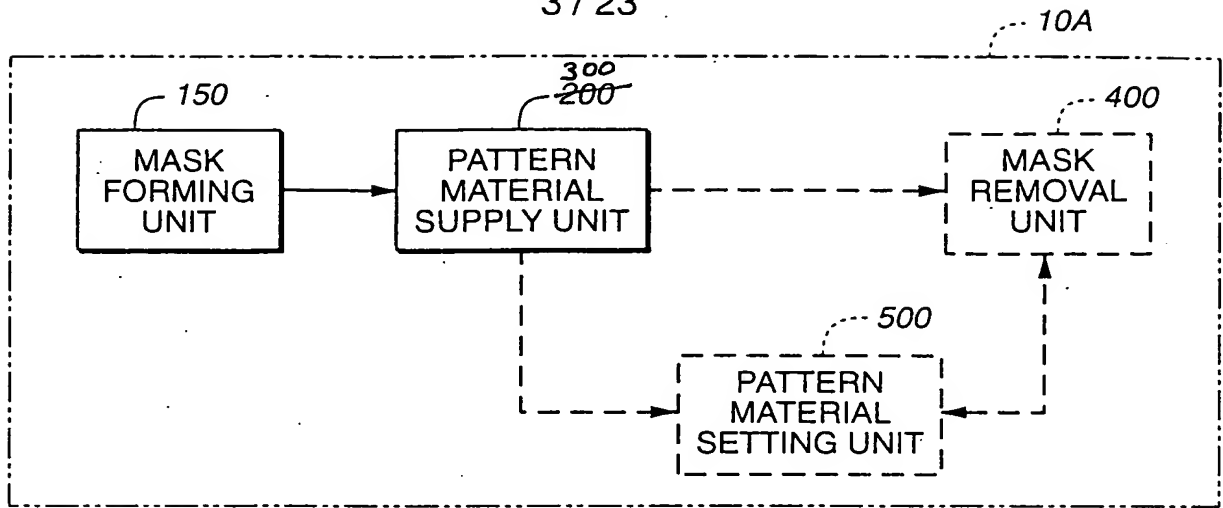


**FIG.\_1**

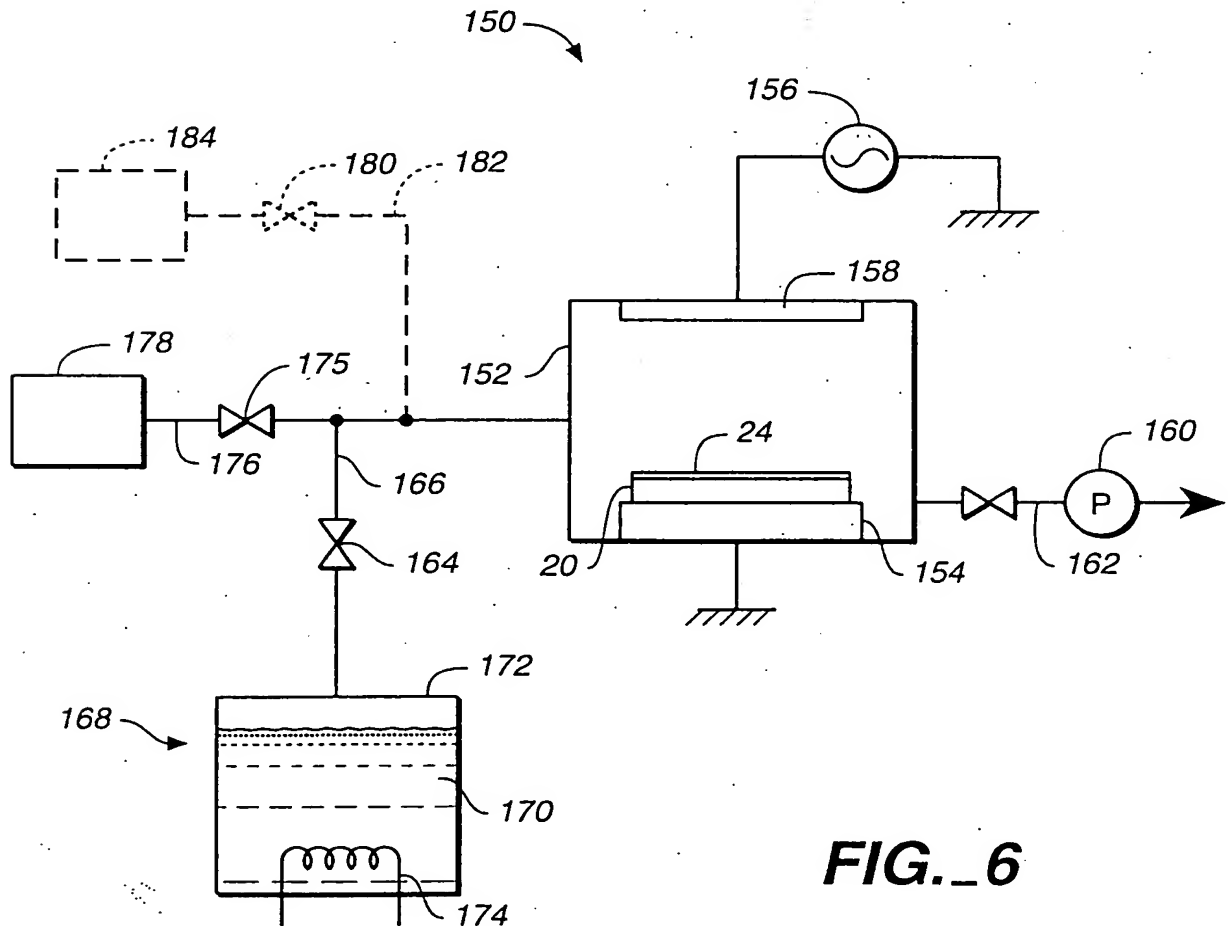


**FIG.\_2**

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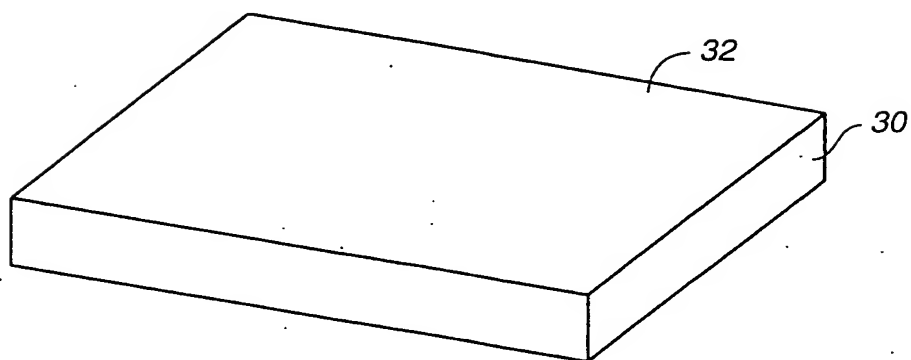


**FIG.\_5**

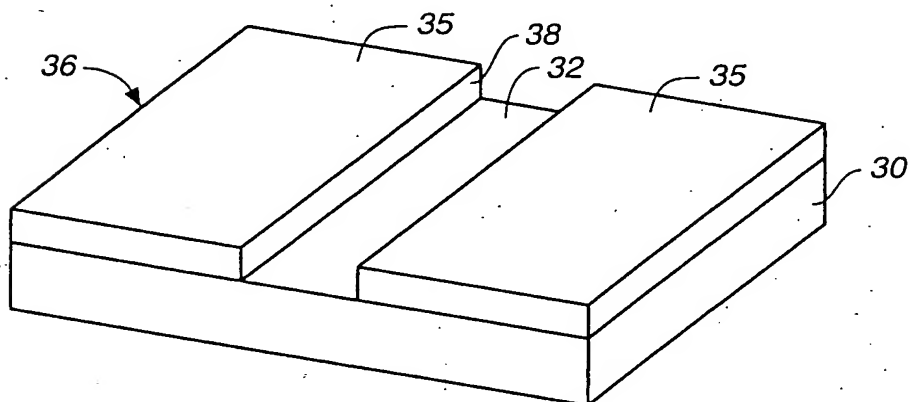


**FIG.\_6**

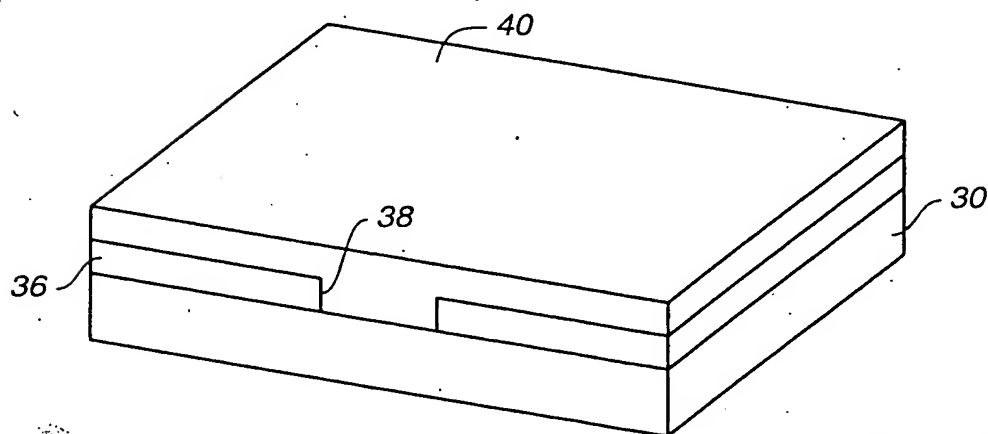
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**FIG. 18#(1)**



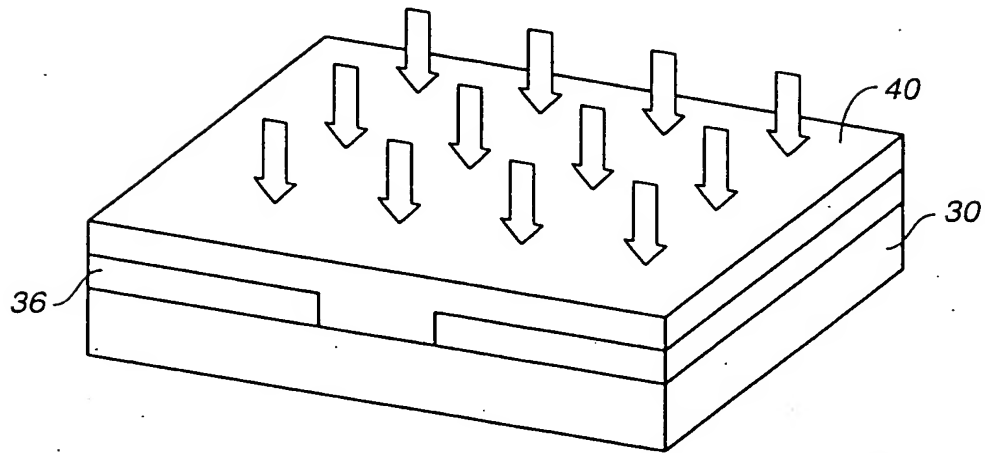
**FIG. 18#(2)**



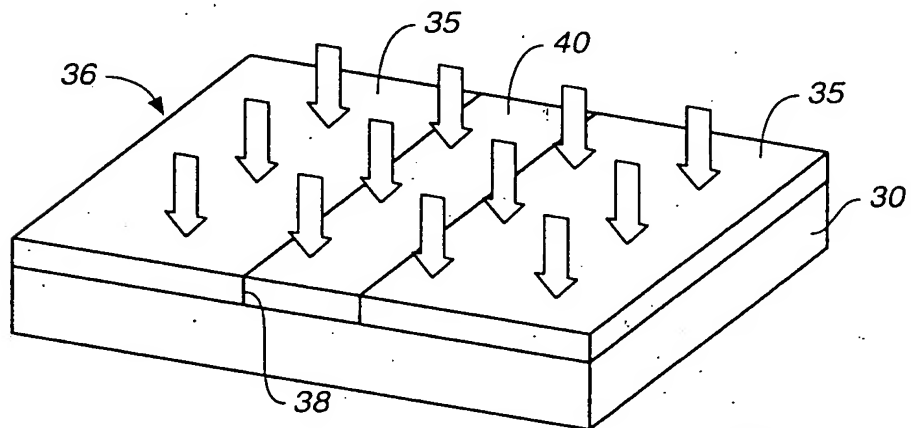
**FIG. 18#(3)**



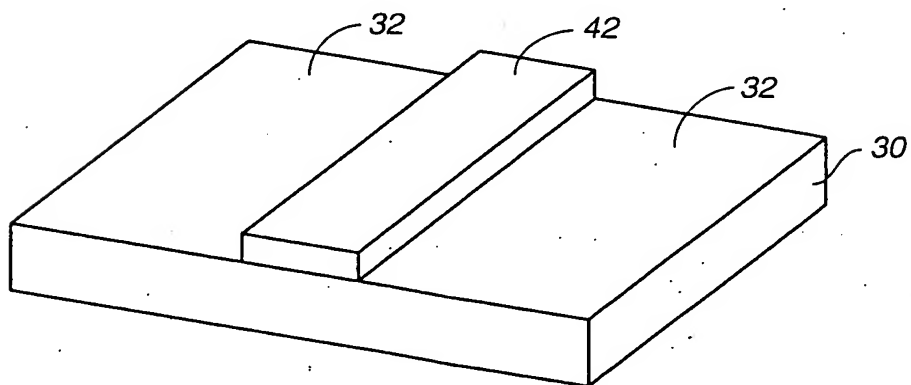
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**FIG. 19(1)**



**FIG. 19(2)**

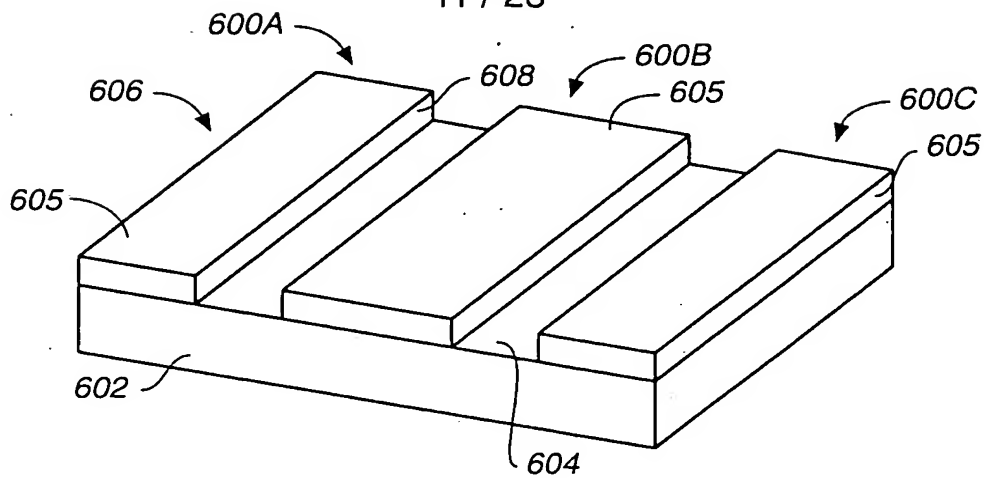


**FIG. 19(3)**

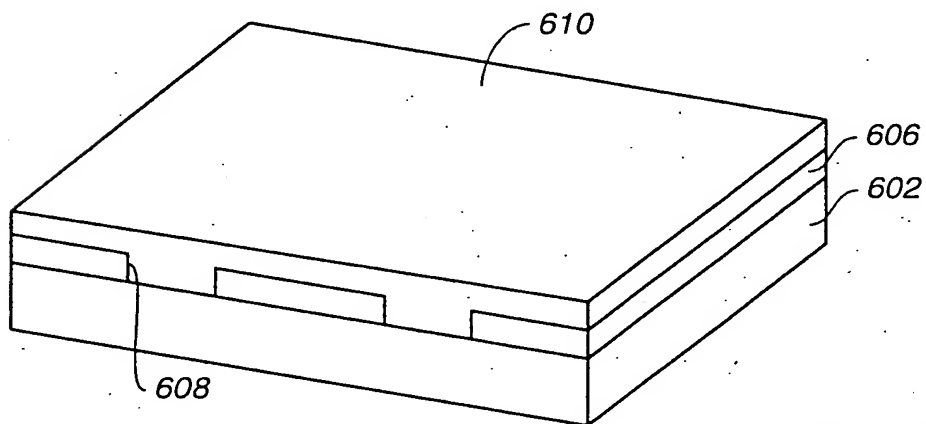




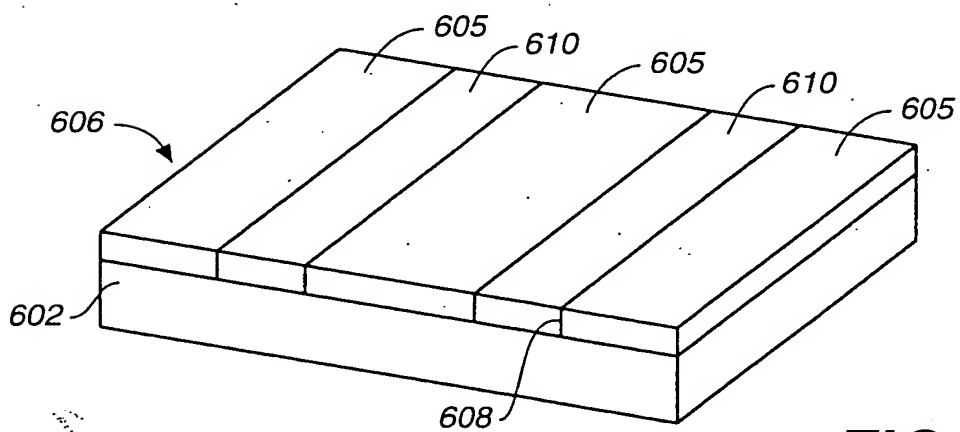
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**FIG. 20#(1)**



**FIG. 20#(2)**

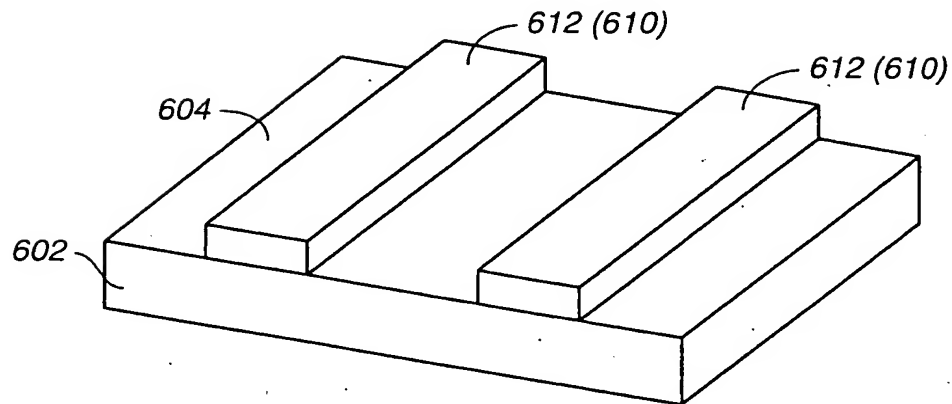


**FIG. 20#(3)**

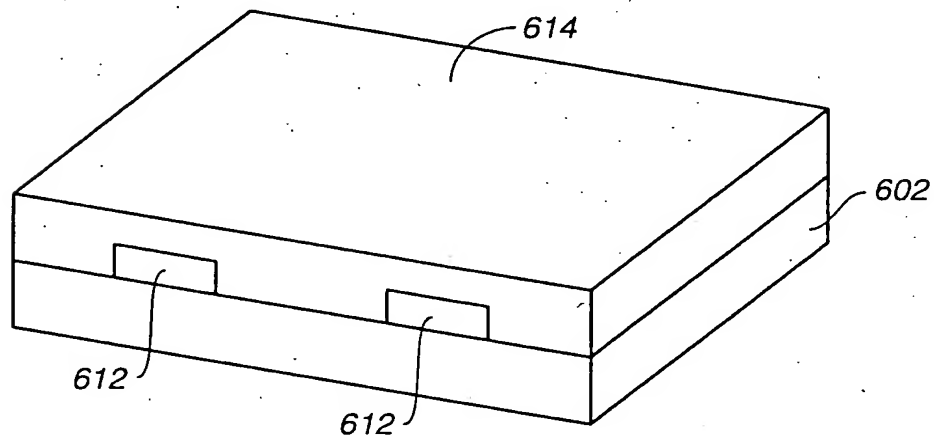




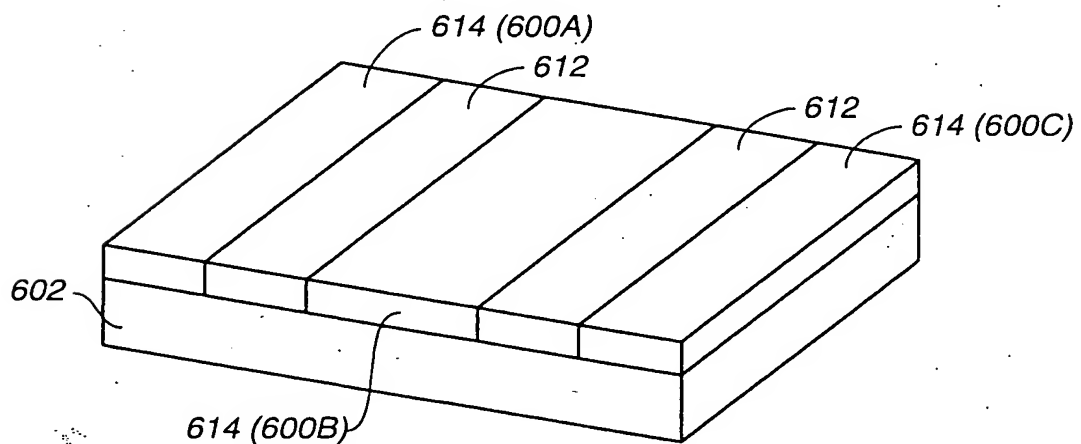
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**FIG. 21(1)**



**FIG. 21(2)**

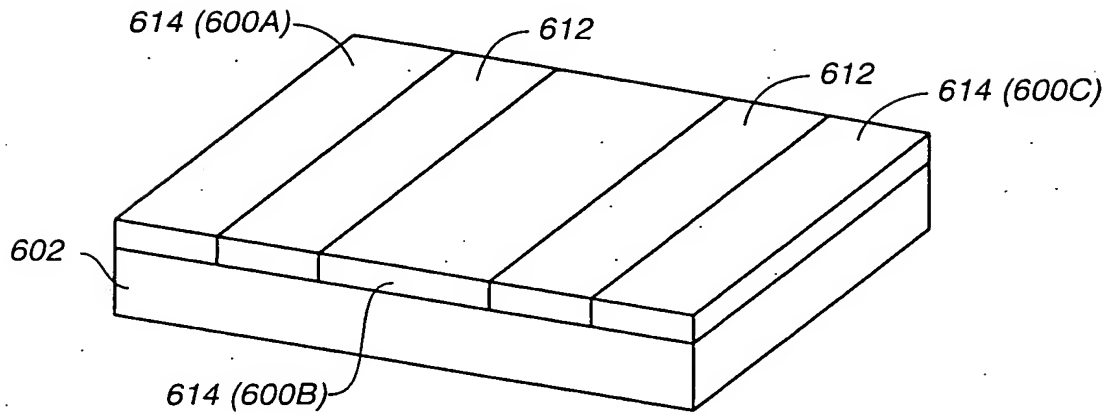


**FIG. 21(3)**

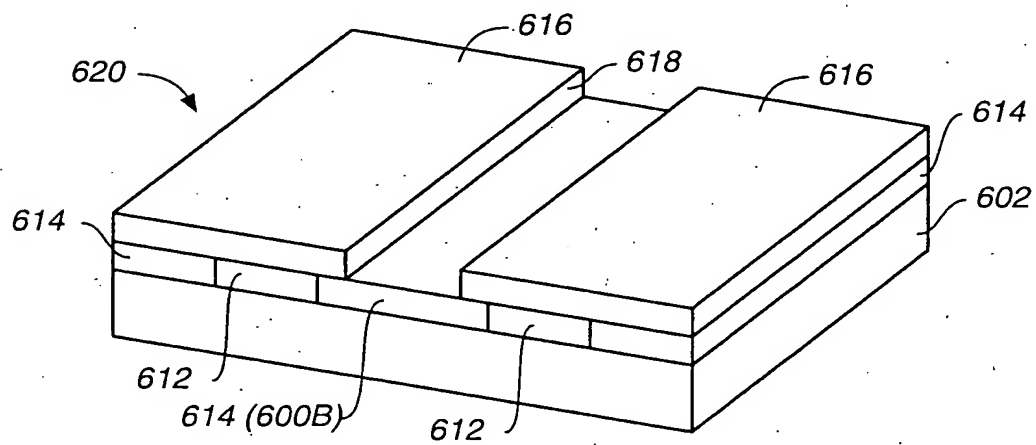




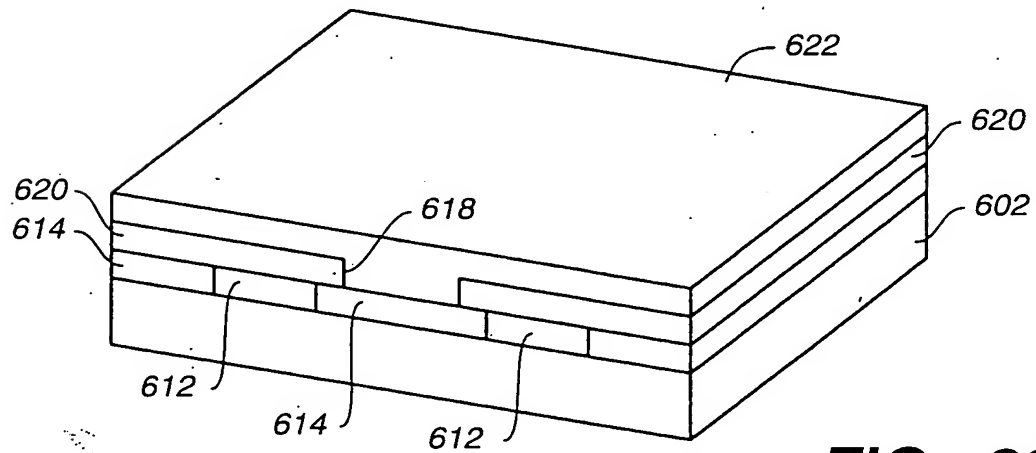
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**FIG. 22(1)**



**FIG. 22(2)**

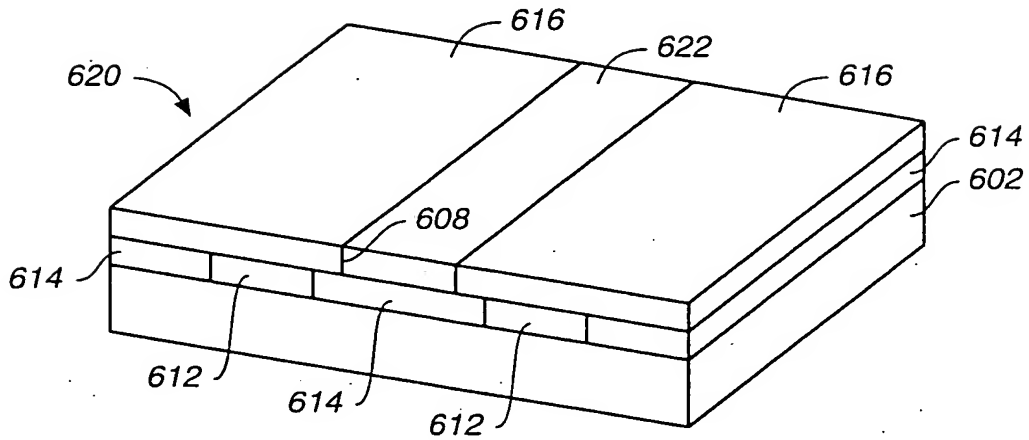


**FIG. 22(3)**

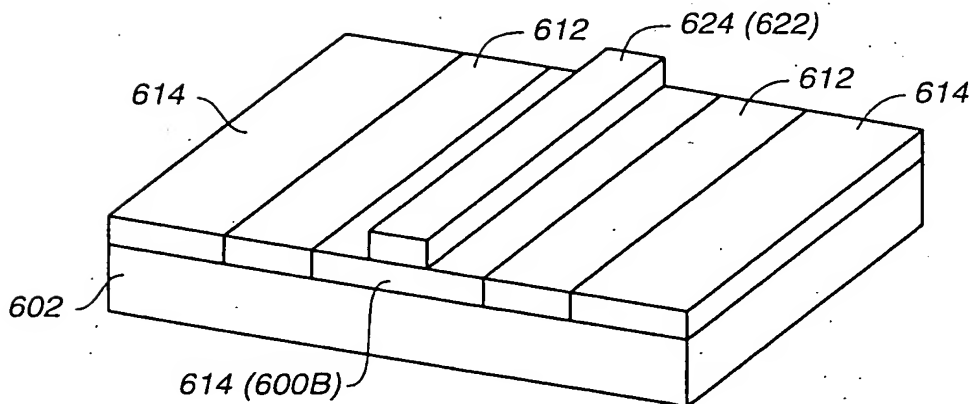




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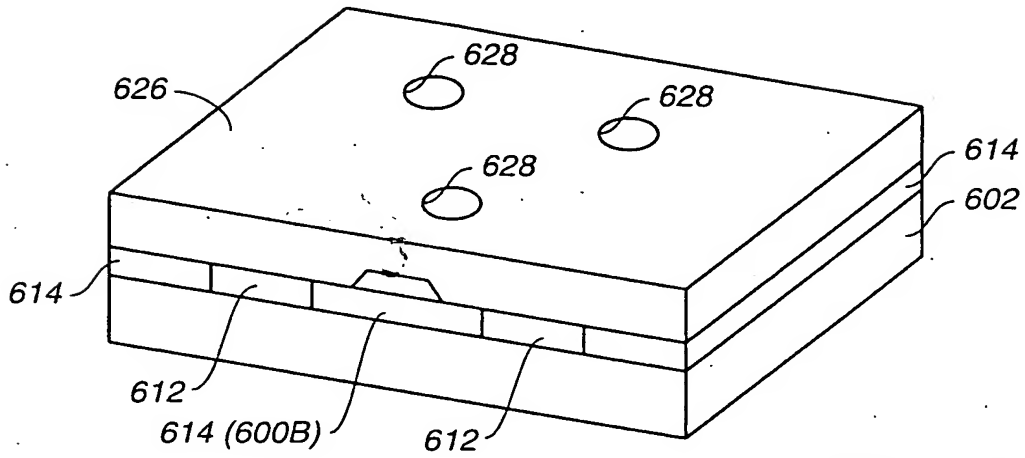


**FIG. 23(1)**

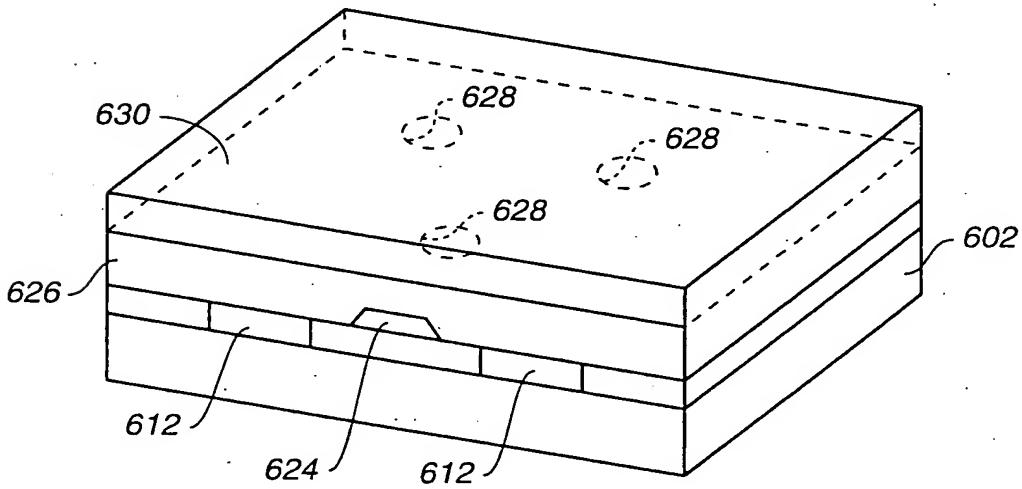


**FIG. 23(2)**

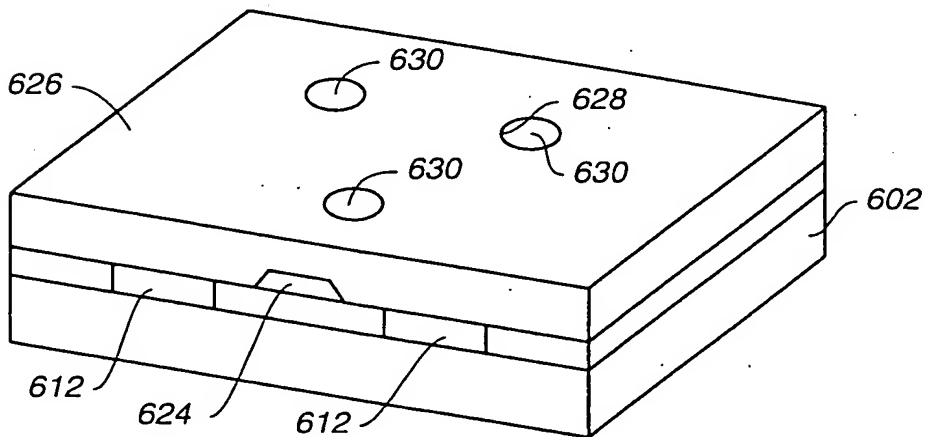
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**FIG. 24(1)**

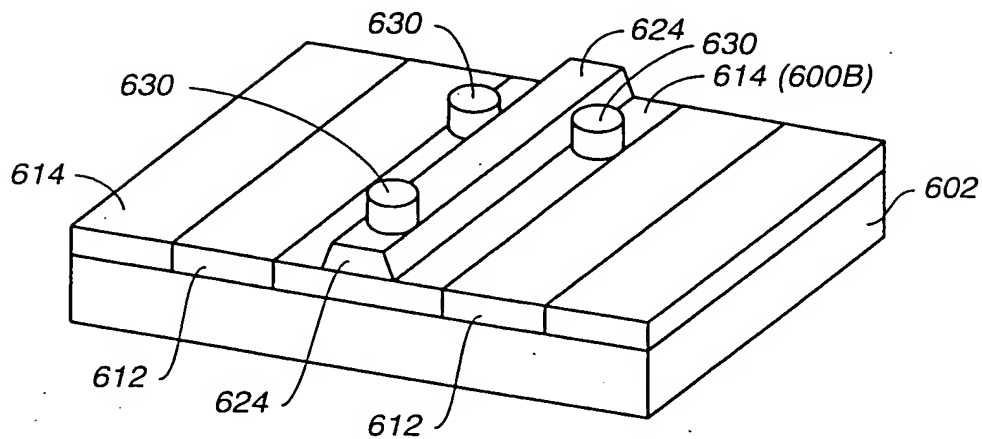


**FIG. 24(2)**

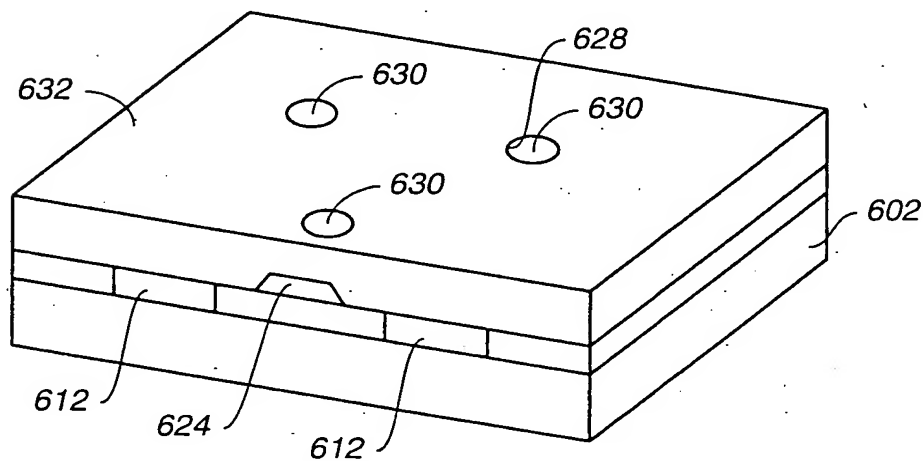


**FIG. 24(3)**

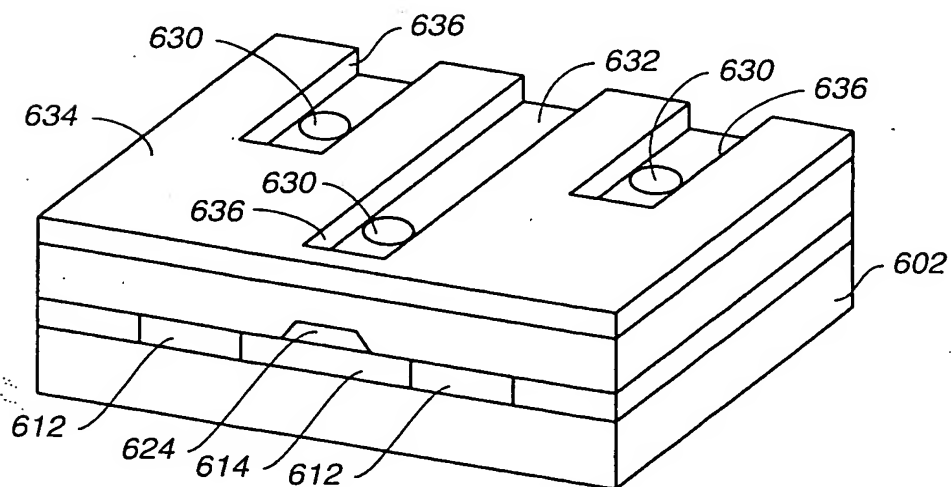
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**FIG. 25#(1)**

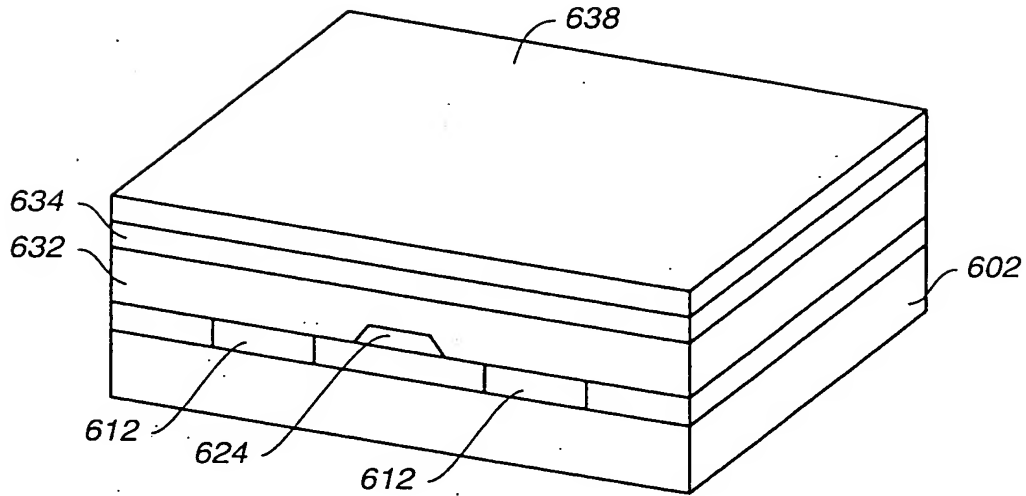


**FIG. 25#(2)**

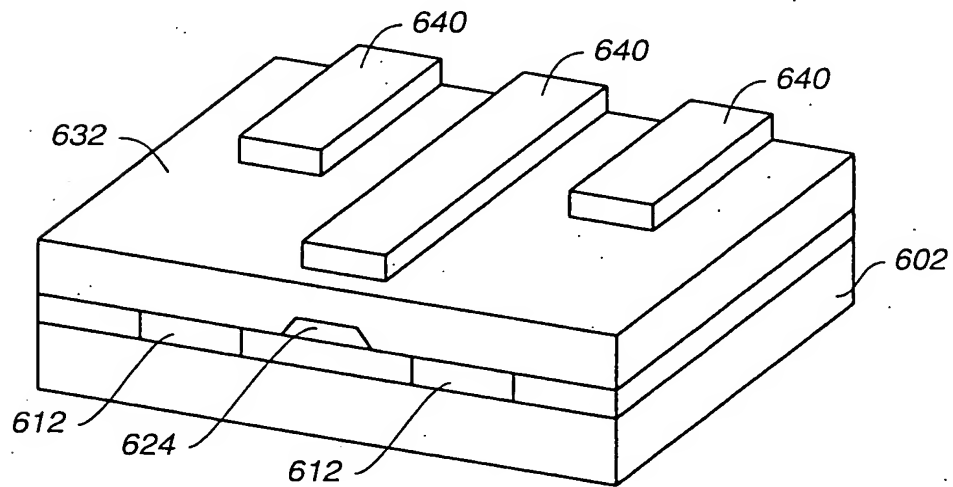


**FIG. 25#(3)**

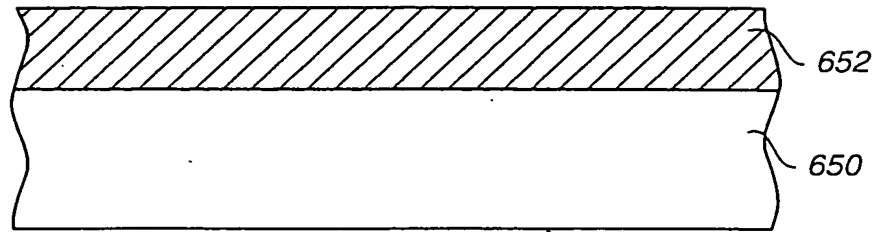
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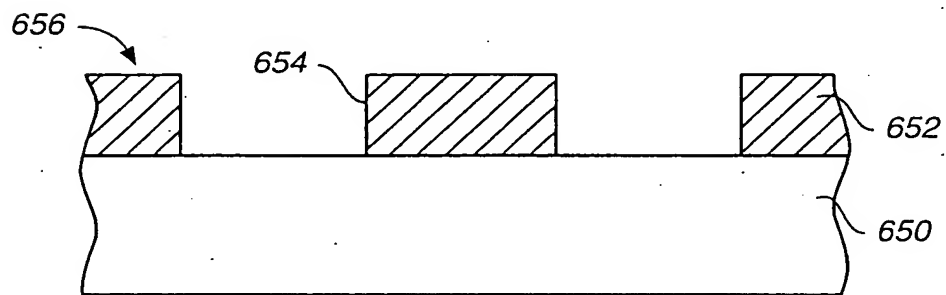
**FIG. 26#(1)**



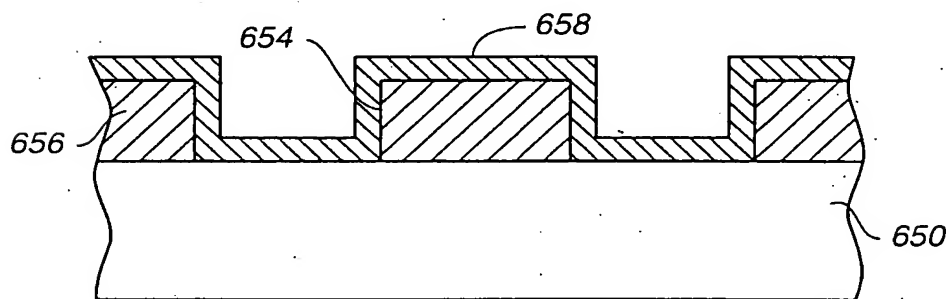
**FIG. 26#(2)**



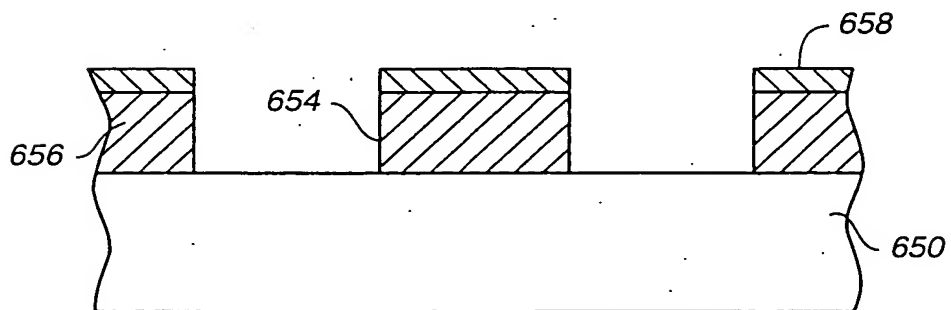
**FIG. 27#(1)**



**FIG. 27#(2)**



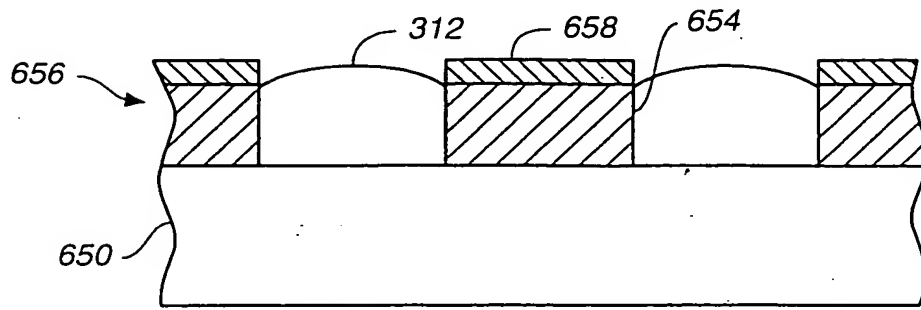
**FIG. 27#(3)**



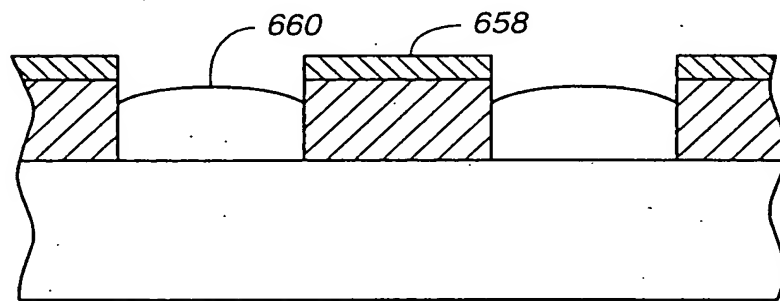
**FIG. 27#(4)**



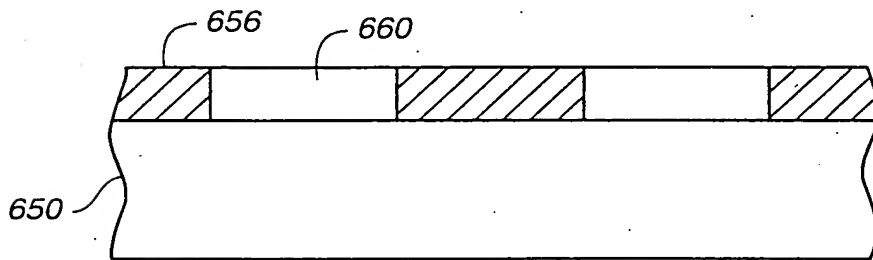
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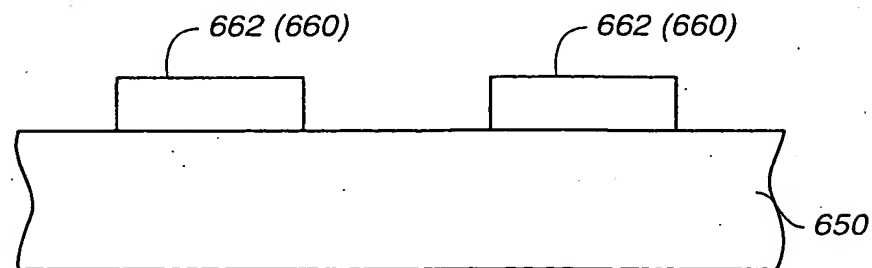
**FIG. 28(1)**



**FIG. 28(2)**



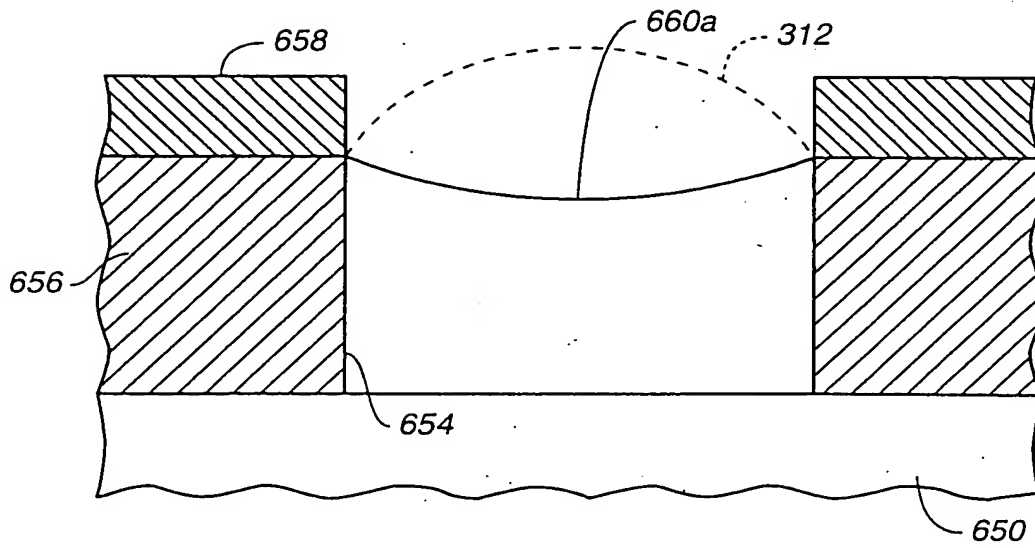
**FIG. 28(3)**



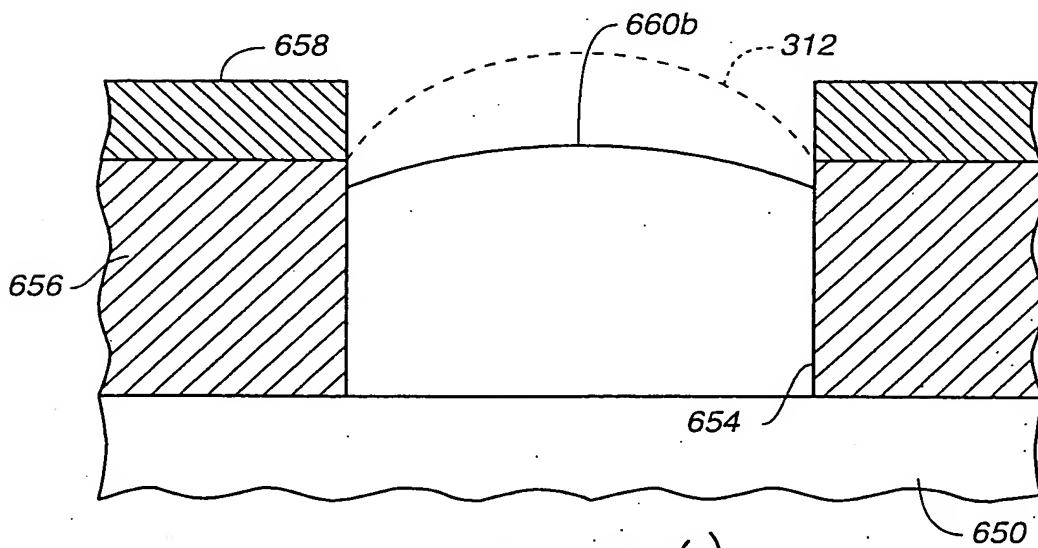
**FIG. 28(4)**



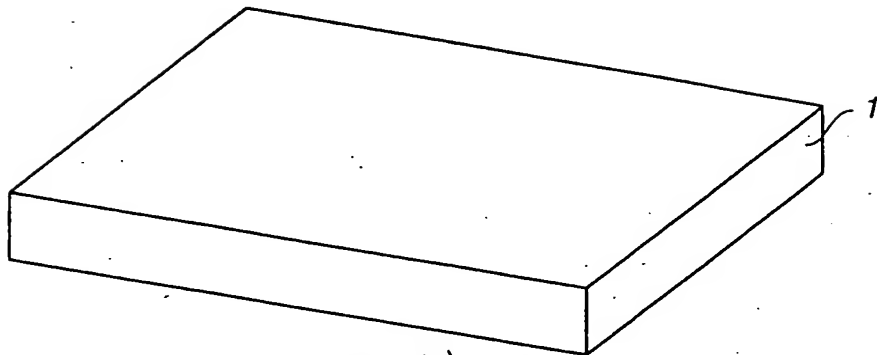
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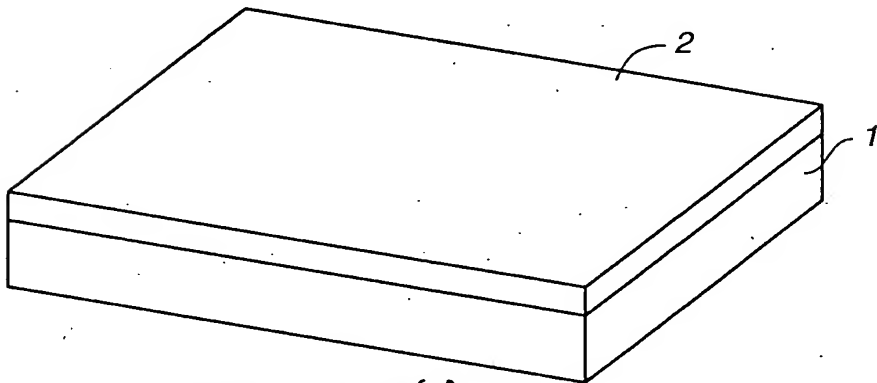
**FIG. 29#(1)**



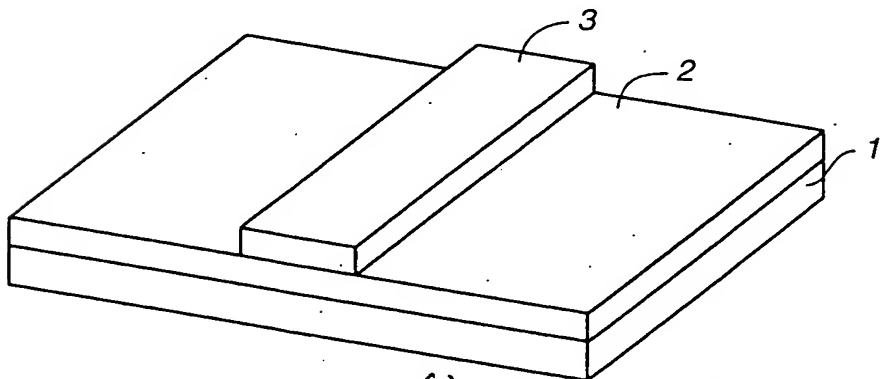
**FIG. 29#(2)**



**FIG. 33#(1)** Prior Art



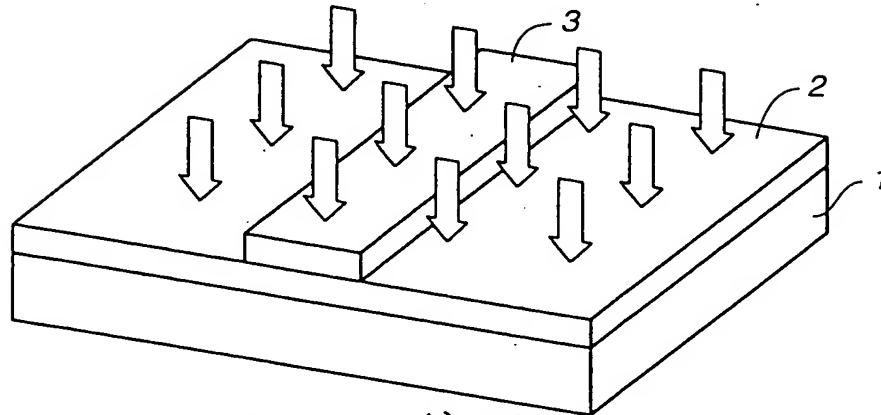
**FIG. 33#(2)** Prior Art



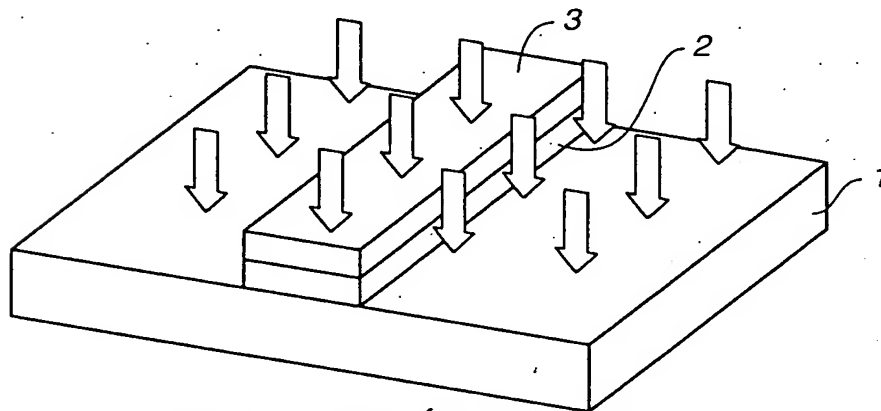
**FIG. 33#(3)** Prior Art



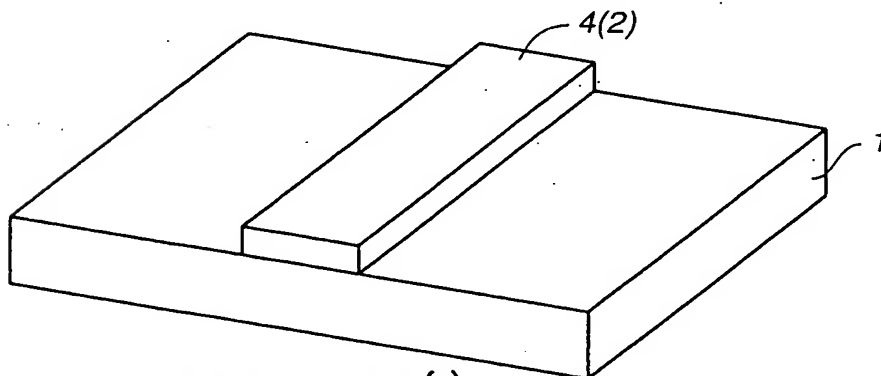
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**FIG. 34#(1)** Prior Art



**FIG. 34#(2)** Prior Art



**FIG. 34#(3)** Prior Art